

A Software Approach to Defeating Side Channels in Last-Level Caches Ziqiao Zhou, Michael K. Reiter, Yinqian Zhang ziqiao@cs.unc.edu University of North Carolina

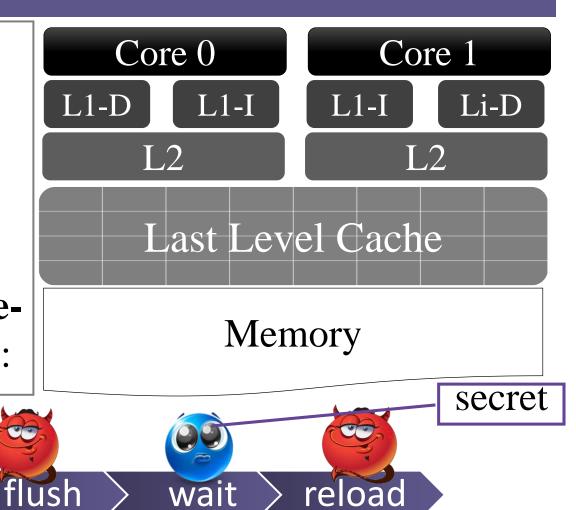
Goals

Last-Level Cache-based side channels exploit imperfect hardware isolation to leak information between different security domains, of which two fine-grained varieties are primeprobe and flush-reload attacks:

wait

reload

flush

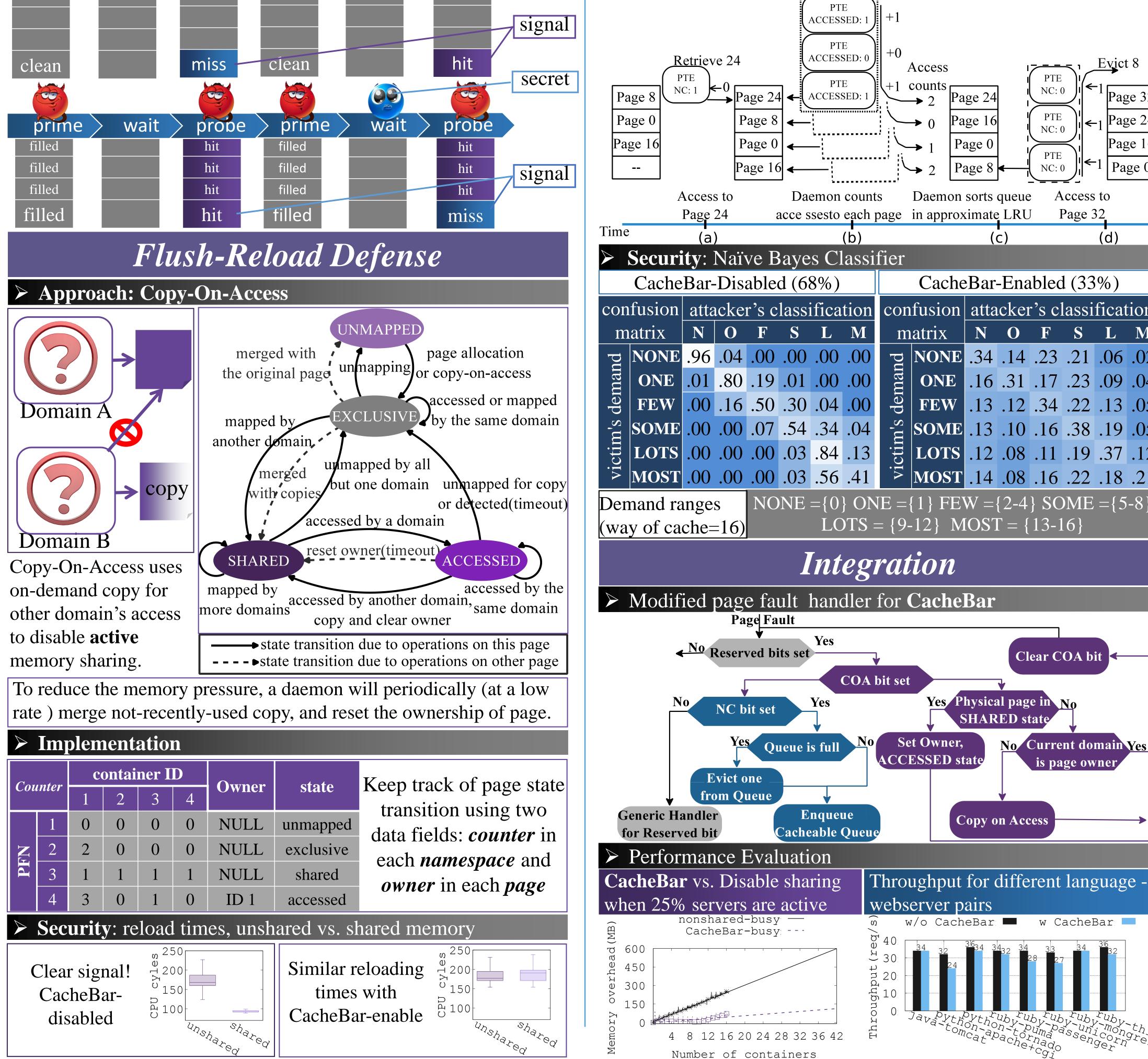


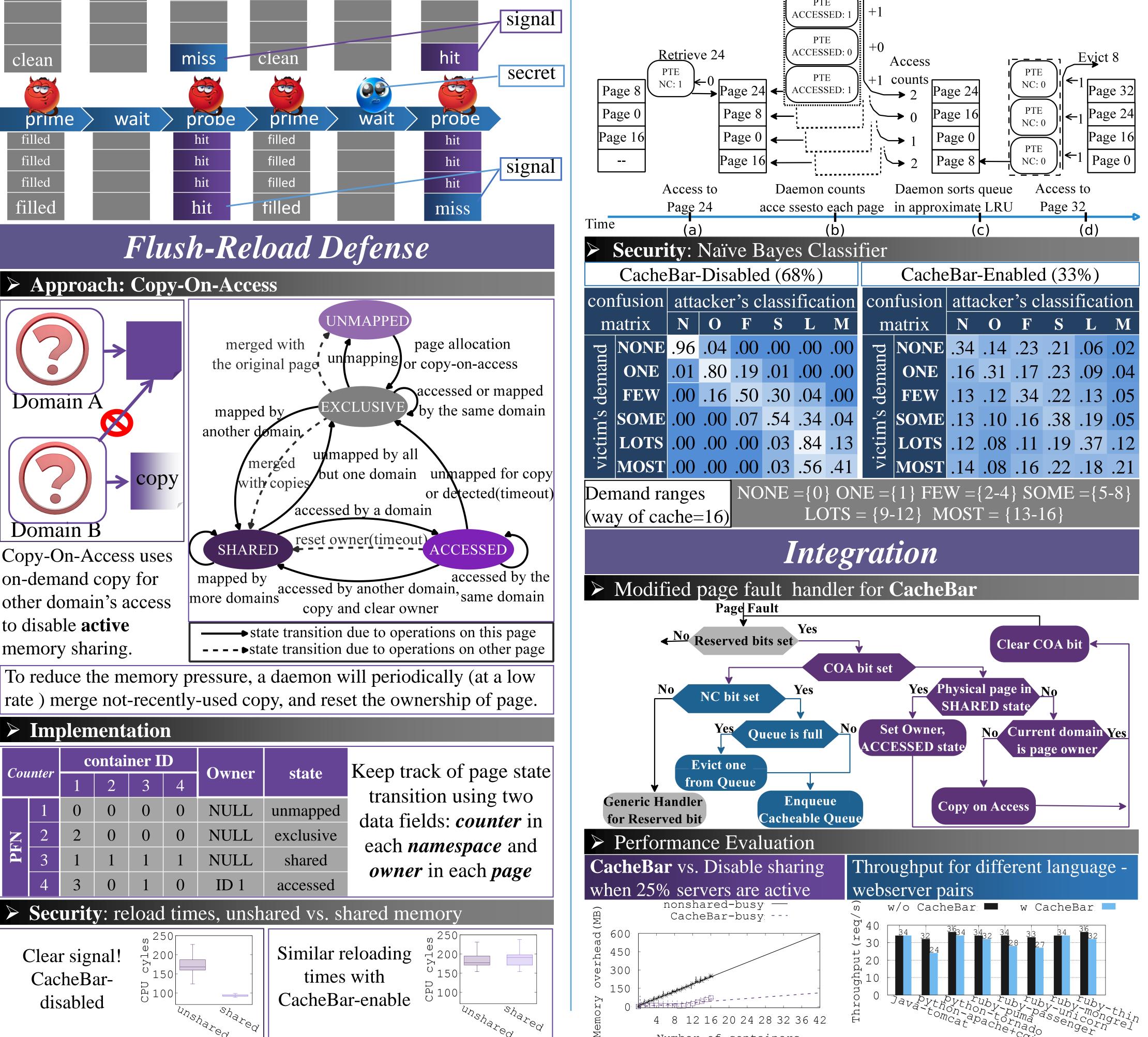
Prime-Probe Defense

Approach: Cacheable Queue Available budget (*k*): \checkmark < # way of LLC Dynamically changed Non-cacheable Cacheable

Control # of cacheable memory pages (*k*) per *color* and per *domain*, to disable the ability Independently chosen to prime whole cache set. On access to NC page, LRU strategy is used for cacheable page replacement. In addition, three properties (\checkmark) are used to guarantee the security.

Implementation: a **queue** for each page color in each domain





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Cloud Security Horizons Summit, March 2016